torney Docket No.: MP011

IN THE UNITED STATES PATENT AND TRADEMARK

Applicant: Sehat Sutardja

Serial No.: 09/966,914

Filed Title

: September 27, 2001

: IMPROVED INTEGRATED CHIP PACKAGE HAVING INTERMEDIATE

Art Unit : 2826

Examiner: Alexander Willia

SUBSTRATE

Commissioner for Patents Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Applicant submits the reference listed on the attached form PTO-1449, a copy of which is enclosed.

This statement is being filed under 37 CFR 1.97(c) before the mailing date of a Final Office Action or a Notice of Allowance. No certification is submitted, therefore a submission of credit card payment in the amount of \$180.00 is provided for the fee. Please apply any other charges or credits to Deposit Account No. 50-1236.

01/22/2003 HVUONG1 00000062 09966914

01 FC:1806

180.00 OP

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

January 15, 2003

Date of Deposit

Signature

Joseph L Stevenson

Typed or Printed Name of Person Signing Certificate

Attorney's Docket No.: MP0115

Applicant: Sehat Sutardja Serial No.: 09/966,914

Filed: September 27, 2001

Page: 2

Respectfully submitted,

Date: Vanuary 15, 2003

Joseph L. Stevenson Reg. No. 43,163

Please address all correspondence to:

Eric B. Janofsky

General Patent Counsel

Marvell Semiconductor, Inc.

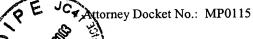
700 First Avenue, Mail Stop 509

Sunnyvale, CA 94089

General Telephone Number (408) 222-2500

Facsimile (408) 752-9034

Customer No. 23624



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit : 2826 pplicant: Sehat Sutardia

Examiner: Alexander Williams Serial No.: 09/966,914

Filed : September 27, 2001

: IMPROVED INTEGRATED CHIP PACKAGE HAVING INTERMEDIATE Title

SUBSTRATE

Commissioner for Patents Washington, D.C. 20231

STATEMENT OF NON-LISTED REFERENCE

Applicant submits that in the 7/31/02 Office Action the Examiner cited US Patent No. 5,311,402 (Kobayashi), but did not include the Kobayashi reference on the PTO-892 form. respectfully requests that the Examiner list the Kobayashi Applicant has included a copy of reference on the PTO-892 form. the Kobayashi reference for the Examiner's convenience.

Respectfully submitted,

anuary 15, 2003

Stevenson Reg. No. 43,163

Please address all correspondence to:

Eric B. Janofsky General Patent Counsel

Marvell Semiconductor, Inc.

700 First Avenue, Mail Stop 509

94089 Sunnyvale, CA

General Telephone Number (408) 222-2500

Facsimile (408) 752-9034

Customer No. 23624

CERTIFICATE OF MAILING BY FIRST CLASS MAIL .

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

January 15, Date of Deposit

Joseph L Stevenson

Typed or Printed Name of Person Signing Certificate